## **Abstract**

A computer system including a switch box is provided for controlling the selective activation and deactivation of the computer systems' memory storage devices. The switch box includes one or more pairs of switches with each pair including a power switch and a signal switch. The power switch selectively provides power to the corresponding power switches. Meanwhile, the signal switch selectively provides an open or closed circuit for allowing or restricting the transmission of data signals between the computers processor and memory storage device.